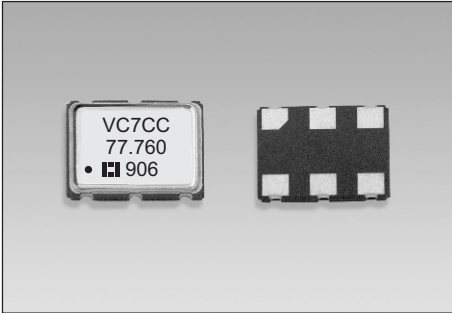




## • VC7CC Series



The VC7CC series is surface mount voltage controlled crystal oscillator designed primarily for use in phase locked loops, phase shift keying and other telecommunication applications such as ADSL, Cable modem etc.

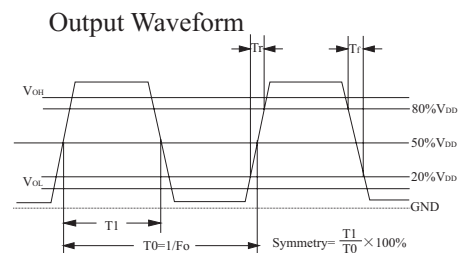
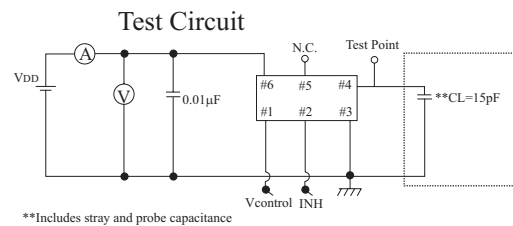
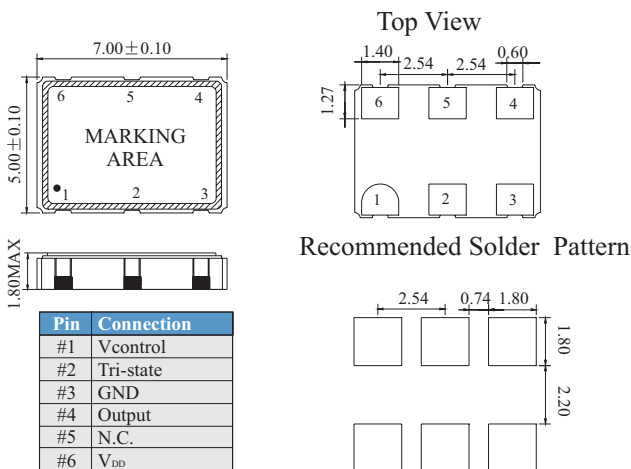
### FEATURES

- Miniature Package
- Industry Standard
- CMOS output
- Tri-State Enable/Disable
- Tape and Reel
- 3.3V operation

## Electrical Specifications

| Parameter                   | Condition                  | VC7CC                                     |                        |
|-----------------------------|----------------------------|---|------------------------|
| Frequency Range             | F <sub>0</sub>             | 50.000MHz~200.000MHz                      |                        |
| Frequency Calibration       | At 25°C                    | ± 15ppm                                   |                        |
| Temperature Stability       | Over T <sub>OPR</sub>      | ± 15ppm, ± 25ppm, ± 50ppm                 |                        |
| Stability vs. power change  | V <sub>DD</sub> ±5%        | ± 5ppm                                    |                        |
| Stability vs. load change   | 15pF±10%                   | ± 3ppm                                    |                        |
| Pullability                 | Over Control Voltage Range | ± 50ppm, ± 100ppm, ± 200ppm               |                        |
| Control Voltage Range       |                            | 0~3.3V                                    |                        |
| Operating Temperature Range | T <sub>OPR</sub>           | 0°C~70°C (-40°C~+85°C option)             |                        |
| Storage Temperature Range   | T <sub>STG</sub>           | -55°C~+125°C                              |                        |
| Power Supply Voltage        | V <sub>DD</sub>            | 3.3V±5%                                   |                        |
| Aging (First Year)          | 25°C ± 3°C                 | ± 2ppm Max                                |                        |
| Supply Current              | I <sub>DD</sub>            | 50.000MHz to 95.999MHz                    | 30mA Max               |
|                             |                            | 96.000MHz to 200.000MHz                   | 40mA Max               |
| Output Symmetry             | Sym                        | 40/60%(45/55% Option)                     |                        |
| Rise time                   | T <sub>r</sub>             | 20%V <sub>DD</sub> ~80%V <sub>DD</sub>    | 1nS Max                |
| Fall Time                   | T <sub>f</sub>             | 80%V <sub>DD</sub> ~20%V <sub>DD</sub>    | 1nS Max                |
| Output Voltage              | V <sub>OH</sub>            |   | 90%V <sub>DD</sub> Min |
|                             | V <sub>OL</sub>            |   | 10%V <sub>DD</sub> Max |
| Output Load                 |                            |   | 15pF Max               |
| Start-up Time               | T <sub>s</sub>             |   | 10mS Max               |
| Pin 2, tri-state function   |                            | Pin 2=H or open....Output active at pin 4 |                        |
|                             |                            | Pin 2=L....high impedance at pin 4        |                        |
| Packing Unit                |                            |   | 1000pcs/reel           |

## Mechanical Dimensions(mm)



\*\*\*note: A 0.01µF bypass capacitor should be placed between V<sub>DD</sub>(Pin6) and GND(Pin3) to Minimize power supply line noise

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Specifications are subject to change without notice